

Credits: 2

Outline:

1. P-N junction diode
 - a. I~V characteristics, b) half wave rectifier, c) full wave rectifier d) clipper
2. Bipolar Junction Transistor
 - a. Transistor characteristics , Transistor application as an amplifier (Common Emitter configuration)
3. Truth table verification – Universal gate realization
4. Adder/subtractor
5. seven segment display interface to adder/subtractor.
(Circuit level simulation of above experiments using circuit simulator)